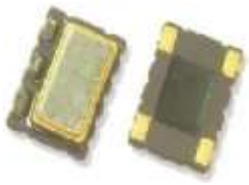
Series VT7

7.0 x 5.0 mm SMD Voltage Contro led Temperature Compensated Crystal Osci lator

FEATURE

- Typical 7.0x5.0x1.9 mm ceramic SMD package.
- For automatic assembly.
- Compactness and light weight.
- Low power consumtion.
- VCTCXO available.

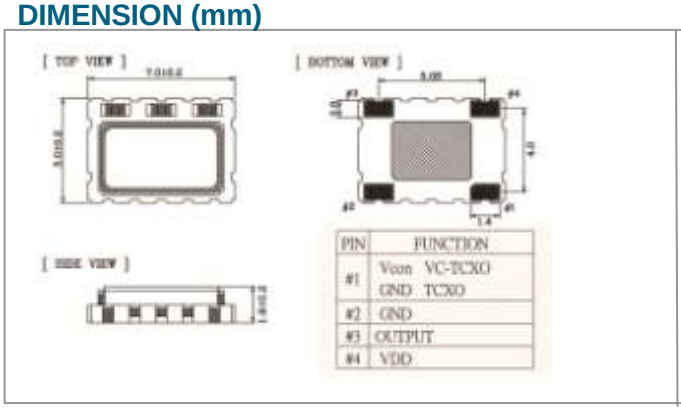


TYPICAL APPLICATION

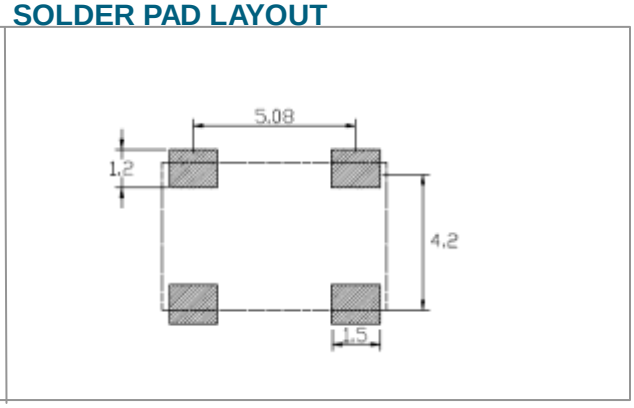
- Femtocell, Base Stations
- WLAN/WiMAX/WIFI, Wireless Communications
- Mobile Phone

RoHS Compliant Standard

DIMENSION (mm)



SOLDER PAD LAYOUT



ELECTRICAL SPECIFICATION

Parameter	3.3V		2.5V		unit
Supply Voltage Variation (VDD) 5%	3.135	3.465	2.4	2.75	V
Frequency Range	5	52	5	52	MHz
Standard Frequency (for CMOS)	5, 6.4,8, 8.192,10,12.5,12.8,16,16.384,19.44,25, 26, 40				
Standard Frequency (for Clipped Sine)	8, 8.192,10,12.5,12.8,16,16.384,19.44,25, 26, 40				
Frequency Tolerance*	-	±2.0	-	±2.0	ppm
Frequency Stability					
Vs Supply Voltage (±5%) change	-	±0.1	-	±0.1	ppm
Vs Load (±10%) change	-	±0.2	-	±0.2	
Vs Aging	-	±1.0	-	±1.0	
Supply Current (CMOS output)	-	6	-	6	mA
Supply Current (Clipped Sine Wave)	-	3.5	-	3.5	
Output Level (CMOS)					
Output High (Logic "1")	90%VDD	-	90%VDD	-	V
Output Low (Logic "0")	-	10%VDD	-	10%VDD	
Duty	45	55	45	55	%
Output Level (Clipped Sine Wave)	0.8	-	0.8	-	Vp-p
Load (CMOS)	15pF		15pF		
Load (Clipped Sine Wave)	10KΩ//10pF		10KΩ//10pF		
Control Voltage Range (VCTCXO)	0.5	2.5	0.5	2.5	V
Pulling Rage (VCTCXO)	±5.0	±12.0	±5.0	±12.0	ppm
Vc Input Impedance (VCTCXO)	100	-	100	-	KΩ
Phase Noise @ 12.8 MHz					
100 Hz	-115		-115		dBc / Hz
1 KHz	-135		-135		
10 KHz	-148		-148		
Start Time	-	2	-	2	mSec
Storage Temp. Range	-55	125	-55	125	°C

Standard frequencies are frequencies which the crystal has been designed and does no imply a stock position
*Frequency at 25°C, 1 hour after reflow
Packing:Tape & Reel 1000/3000pcs Reel

FREQ. STABILITY vs. TEMP. RANGE

ppm / Temp(°C)	±0.5	±1.0
0~+55	O	O
-10~+60	△	O
-20~+70	O	O
-30~+85	△	O
-40~+85	△"	O

* O:Available △:Conditional X: Not available
* 10~26MHz and Pulling < 8ppm available