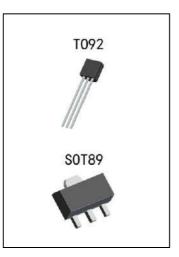


# 100mA Current、 30V Input Voltage、 Low Power LDO

## CY78LXX

#### **General Description**

CY78LXX is three-terminal positive regulators. One of these regulators can deliver up to 100mA of output current. The internal limiting and thermal-shutdown features of the regulator make them essentially immune to overload. When used as are placement for a zener dioderesistor Combination, an effective improvement in output impedance can be obtained, together with lower quiescent current.



CY78LXX

#### **Features**

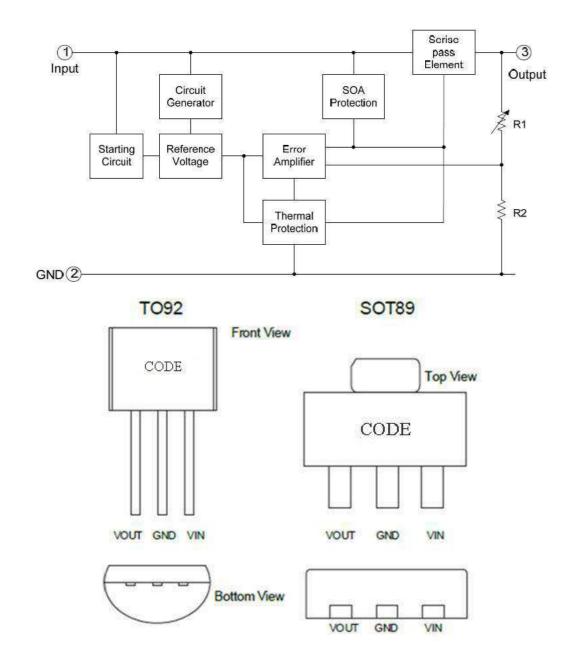
- Output Current of 100mA
- Thermal Overload Protection
- Short Circuit Protection
- Output transistor safe area protection
- No external components
- Package: SOT89 and TO92
- Output voltage accuracy: tolerance ±5%

#### **Order Information**

Part No	Output Voltage	Output Voltage Accuracy
CY78L05	5.0V	±5%
CY78L06	6.0V	±5%
CY78L08	8.0V	±5%
CY78L09	9.0V	±5%
CY78L12	12.0V	±5%

Package	Manner of Packing	Devices per bag/reel
TO92	Bag	1000PCS/bag
SOT89	Reel	1000PCS/reel





### Block Diagram and Pin Arrangement Diagram

### **Pin Assignment**

Pin No.	Pin Name	Description
1	VOUT	Output.
2	GND	Ground connection.
3	VIN	Supply Voltage Input.



### **Functional Description**

CY78LXX is three-terminal positive regulators. One of these regulators can deliver upto 100mA of output current. The internal limiting and thermal-shutdown features of the regulator make them essentially immune to overload. The output voltage can be 5.0V, 6.0V, 8.0V, 9.0V or 12.0V.

#### **Absolute Maximum Ratings**

Parameter	Symbol	Value	Unit
Supply Voltage Input	VIN	30	V
MAX. Output current	I <sub>OUT</sub>	100	mA
MAX. Power SOT-89	P <sub>MAX</sub>	0.5	W
MAX. Power TO-92	P <sub>MAX</sub>	0.625	W
Maximum junction temperature	Tj	-25~125	°C
Storage Temperature	Tstr	-55~125	°C
Soldering temperature and time	Tst	+260(Recommended 10S)	°C

Note:The absolute maximum ratings are rated values exceeding which the product could suffer

physical damage. These values must therefore not be exceeded under any conditions.



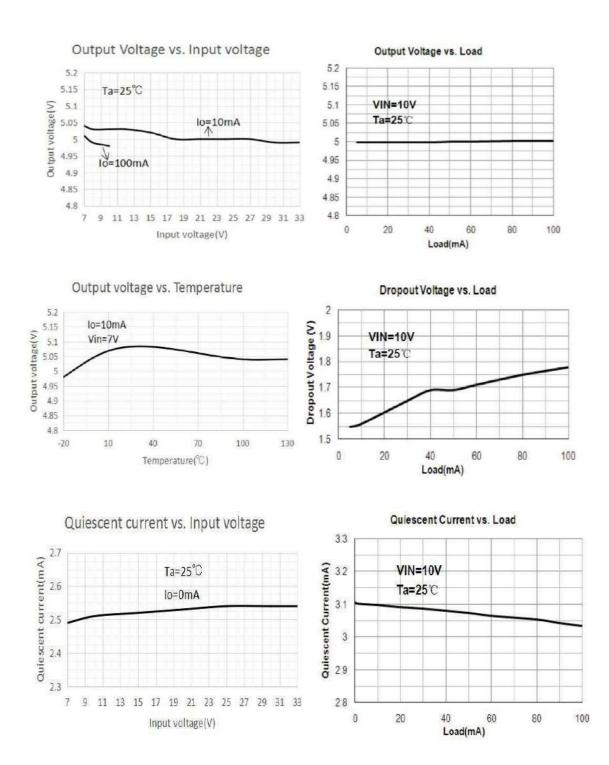
### **Electrical Characteristics**

C <sub>IN</sub> =0.33uF,C₀=0.1uF,0≪Tj≪125℃,	unless	otherwise	noted.
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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
		lo=40mA, V <sub>IN</sub> =10V	0.964*V <sub>OUT</sub>	Vout	1.036*V <sub>OUT</sub>	V
Output	V <sub>OUT</sub>	lo=1mA~40mA V <sub>IN</sub> =7V~18V	0.96*V <sub>OUT</sub>	Vout	1.04*V <sub>OUT</sub>	V
Voltage		lo=1mA~10mA V <sub>IN</sub> =10V	0.95*V <sub>OUT</sub>	Vout	1.05*V <sub>OUT</sub>	V
Line	LNR	V <sub>IN</sub> =7V~18V, Io=20mA	-150	-	150	mV
Regulation	LINK	V <sub>IN</sub> =8V~18V, Io=20mA	-100	-	100	mV
Load		V <sub>IN</sub> =10V, Io=1mA~100mA	-100	-	100	mV
Regulation	V <sub>IN</sub> =10V, Io=1mA~40mA	-30	-	30	mV	
Output noise Voltage	V <sub>N</sub>	F=10Hz to 100KHz	-	40	-	uV/Vo
Ripple Rejection	PSRR	Tj=25℃,f=120Hz, Io=40mA, V <sub>IN</sub> =8V~20V	-	80	-	dB
Quiescent Current	Ι <sub>Q</sub>	V <sub>IN</sub> =10V, I <sub>OUT</sub> =40mA	-	-	5.5	mA
Quiescent	∆lq	V <sub>IN</sub> =8V~18V, I <sub>0</sub> =20mA	-1.5	-	1.5	mA
Current Change	ΔIQ	V <sub>IN</sub> =10V, I <sub>OUT</sub> =1mA~40mA,	-0.1	-	0.1	mA

- LNR: Line Regulation. The change in output voltage for a change in the input voltage. The measurement is made under conditions of low dissipation or by using pulse techniques such that the average chip temperature is not significantly affected.
- LDR: Load Regulation.The change in output voltage for a change in load current at constant chip temperature.





### **Typical Performance Characteristics**



## **Application Circuits**

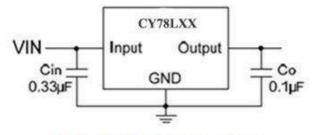
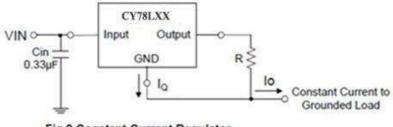


Fig.1 Fixed Output Regulator

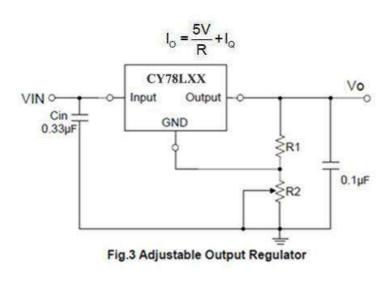
A common ground is required between the input and the output voltages. The input voltage must remain typically 2.0V above the output voltage even during the low point on the input ripple voltage.

- Cin is required if regulator is located an appreciable distance from power supply filter.
- Co is not needed for stability; however, it does improve transient response.





The CY78LXX regulator can also be used as a current source when connected asFig.2. In order to minimize dissipation the CY78LXX is chosen in this application. Resistor R determines the current as follows:



Vo=5V+(5V/R1+lo)\*R2



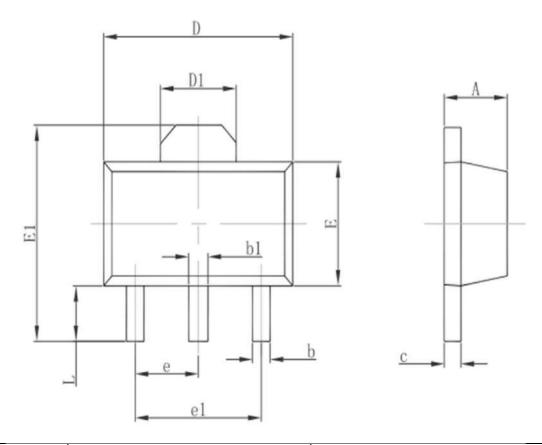
### **Operation Description**

CY78LXX is designed with Thermal Overload Protection that shuts down the circuit when subjected to an excessive power overload condition, Internal Short Circuit Protection that limits the maximum current the circuit will pass, and Output Transistor Safe-Area Compensation that reduces the output short circuit current as the voltage across the pass transistor is increased.

In many low current applications, compensation capacitors are not required. However, it is recommended that the regulator input be bypassed with a capacitor if the regulator is connected to the power supply filter with long wire lengths, or if the output load capacitance is large. An input bypass capacitor should be selected to provide good high frequency characteristics to insure stable operation under all load conditions. A  $0.33\mu$ For larger tantalum, mylar, or other capacitor having low internal impedance at high frequencies should be chosen. The bypass capacitor should be mounted with the shortest possible leads directly across the regulator's input terminals. Normally good construction techniques should be used to minimize groundloops and lead resistance drops since the regulator has no external sense lead.



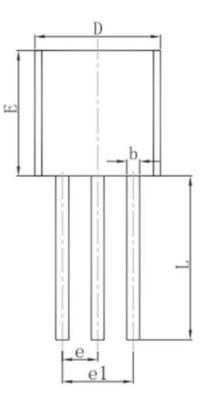
# Package Information (SOT89)

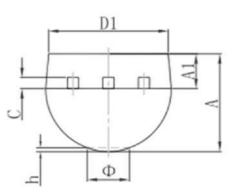


Symbol	Dimensions In Millimeters		Dimensions	s In Inches
Symbol	Min.	Max. Min.		Max.
Α	1.400	1.600	0.055	0.063
b	0.320	0.520	0.013	0.020
b1	0.400	0.580	0.016	0.023
С	0.350	0.440	0.014	0.017
D	4.400	4.600	0.173	0.181
D1	1.550REF.		0.061REF.	
E	2.300	2.600	0.091	0.102
E1	3.940	4.250	0.155	0.167
е	1.500TYP.		0.060TYP.	
e1	3.000TYP.		0.118	BTYP.
L	0.900	1.200	0.035	0.047



# Package Information (TO92)





Symbol	Dimensions I	n Millimeters	Dimensions	s In Inches
Symbol	Min.	Max.	Min.	Max.
Α	3.300	3.700	0.130	0.146
A1	1.100	1.400	0.043	0.055
b	0.380	0.550	0.015	0.022
С	0.360	0.510	0.014	0.020
D	4.300	4.700	0.169	0.185
D1	3.430		0.135	
E	4.300	4.700	0.169	0.185
е	1.270	70TYP. 0.050TYP.		)TYP.
e1	2.440	2.640	0.096	0.104
L	14.100	14.500	0.555	0.571
Φ		1.600		0.063
h	0.000	0.380	0.000	0.015



# **Special Instructions**

The company reserves the right of final interpretation of this specification.

# Version Change Description

Version: V1.8 2021.11.9	Author: Yangyang	Time:
Modify the record:		
1. Re-typesetting the manual ar	d checking some data	